

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 53080
	:	
Shiro SAKIYAMA, et al.	:	Confirmation Number: 6689
	:	
Application No.: 10/511,165	:	Group Art Unit: 2816
	:	
Filed: October 14, 2004	:	Examiner: HILTUNEN, THOMAS J
	:	
For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH REDUCED SPEED VARIATIONS	:	

AMENDMENT FILED WITH RCE

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendment and remarks are submitted in response to the Final Office
Action mailed January 31, 2008.

Amendments to the Claims begin on page 2.

Remarks begin on page 7.